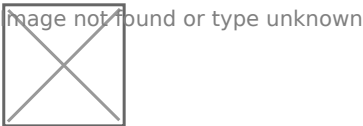


DIY

Q3Cymplefaceeyes

Step 0:

3D

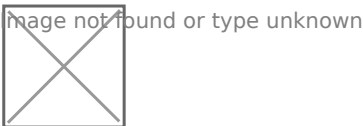


cympleGitHub

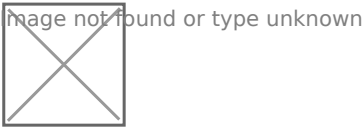
Step 1:

PCB

PCB



32



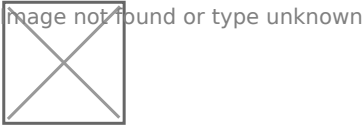
3V3G

PCBPCB.....

Step 2:

<https://thiaha1234.github.io/Thiaha1234SimpleGitHub>

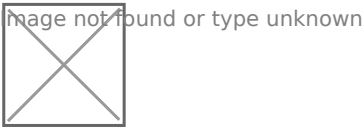
CONNECT



CONNECT

CympleGithubCympleConfigRESET

"Success to connect to WIFI"

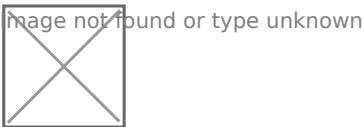
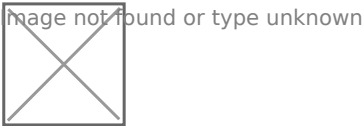


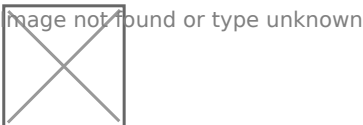
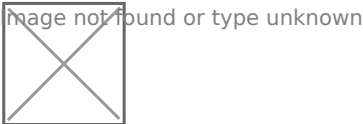
Cymple"Camera failed to init" Cymple1.2 DEV waiting for dwp-filad

Step 3:



Q3







Updated 6 August 2024 09:30:39 by [REDACTED]